

FIG. 21C FORMING n+ REGION

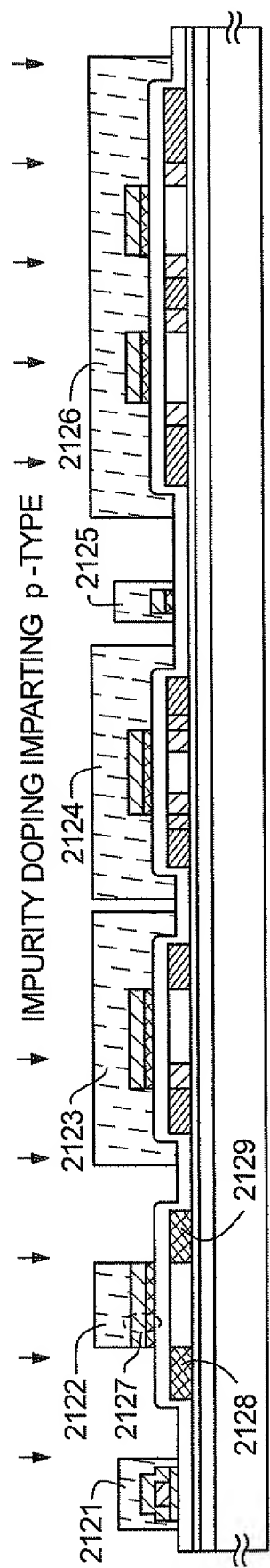


FIG. 21D FORMING p-ch TFT GATE WIRING, FORMING p++ REGION